



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Sheueling Chang Shantz et al.
Title: METHOD AND APPARATUS FOR IMPLEMENTING PROCESSOR
INSTRUCTIONS FOR ACCELERATING PUBLIC-KEY
CRYPTOGRAPHY

Application No.: 10/789,311 Filed: February 27, 2004

Examiner: Not yet assigned Group Art Unit: 2124

Atty. Docket No.: 004-30132

July 21, 2004

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7/21/07
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Respectfully submitted,



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U.S. Department of Commerce, Patent and Trademark Office	Attorney Docket No.: 004-30132
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Application No.: 10/789,311
(Use several sheets if necessary)	Applicant(s): Sheueling Chang Shantz et al.
	Filing Date: February 27, 2004
	Group Art Unit: 2124
	Date Submitted: July 21, 2004

NON PATENT LITERATURE DOCUMENTS

* Examiner Initial	Cite No.	(Including name of author in capital letters, title of article, title of item, date, pertinent pages, volume-issue number(s), publisher, city and/or country where published.)
/CJ/	AA	Intel® Itanium™ Processor, "High Performance On Security Algorithms (RSA Decryption Kernel)," Intel Corporation, 2001, pp. 1-8.
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	AN	F. Morain and J. Olivos, "Speeding Up the Computations on an Elliptic Curve Using Addition-Subtraction Chains," Rapport de Recherche 983, INRIA, France, March 1989, http://citeseer.ist.psu.edu/morain90speeding.html , pp. 119-130.
	AO	
	AP	
	AQ	

Examiner /Carlton Johnson/	Date Considered 06/09/2007
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